

Application No.: 09/752,123

Docket No.: JCLA6706

In The Abstract:

Please substitute the Abstract as follows:

A2
~~[A coprocessor data access control method having a coprocessor memory access instruction with a coprocessor indicating field such that the quantity of word data to be transmitted between the coprocessor and the memory unit can be determined. The coprocessor indicating field actually includes a coprocessor number field and a coprocessor register field. The coprocessor number field indicates the particular coprocessor to be used while the coprocessor register field indicates the particular registers to be used.]~~An apparatus and method for coprocessor data access control and instruction format therewith is provided. The coprocessor data access control method provides an instruction having an indicating field and accesses N data words to or from a memory unit by a specified coprocessor according to the value in the indicating field. The indicating field may have two separate filed including a coprocessor number field and a coprocessor register field. The coprocessor number field contains an information about a specific coprocessor to be activated, and the coprocessor register field contains an information about specific registers to be used.